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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2891 Examiner: Asok K. Sarkar

In Re PATENT APPLICATION of:

Applicant(s): Kun-Hong Chen

Serial No.: 10/812,961

Filing date: March 31, 2004

For: METHOD FOR FORMING A CMOS.

TRANSISTOR

Atty. ref.: SUND 508

**AMENDMENT** 

February 9, 2006

## **Mail Stop Amendment**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on December 13, 2005, please amend the above-identified application as follows: